

In re the Application of: OHASHI, Hitoshi et al.

Group Art Unit: 1762

1 1765 B

Serial No.: 10/047,992

Examiner: Brian K. TALBOT

Filed: January 17, 2002

P.T.O. Confirmation No.: 5363

For. METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING THE SEMICONDUCTOR DEVICE USING SUBSTRATE JIG

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: October 22, 2007

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated April 20, 2007 for three menths, from July 20, 2007 to October 22, 2007, with October 20 and October 21 being a Saturday and Sunday.

Please charge the amount of \$1,050.00 to cover the cost of the extension for a large entity to Deposit Account No. 01-2340.

Respectfully submitted,

KRATZ, QUINTOS & HANSON, LLP

Darren Crew

Attorney for Applicants Reg. No. 37,806

DC/ak/xl Atty. Docket No. **020052** Suite 400 1420 K Street, N.W. Washington, D.C. 20005 (202) 659-2930

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PATENT TRADEMARK OFFICE

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